

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Chamberlain et al. § Group Art Unit: 2185
§ Confirmation No.: 7236
§
Serial No.: 10/814,736 § Examiner: Campos, Yaima
Confirmation No.: 7236 §
Filed: March 31, 2004 § Attorney Docket No. GB920030066US1
§
Title: Memory Allocation § IBM Corporation, IP Law Department
§ 11400 Burnet Rd.
§ Austin, Texas 78758

Certificate of Electronic Transmission Under C.F.R. §1.8(a)

I hereby certify that this correspondence was transmitted electronically to the United States Patent & Trademark Office on **July 22, 2006**.

By: /Leslie A. Van Leeuwen, Reg. No. 42,196/
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July 22, 2006
Date

SUBSTANCE OF THE INTERVIEW
PURSUANT TO MPEP 713.04

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

A. INTRODUCTORY COMMENTS

In response to the Examiner's Interview Summary mailed to Applicants on July 11, 2006 and in light of the fact that a formal written reply to the last Office Action has already been timely filed, Applicants now respectfully submit their written statement of the Substance of the Interview pursuant to 37 CFR § 1.133(b) and MPEP 713.04.

No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and the undersigned hereby authorizes the Commissioner to charge any fees for this extension to IBM Corporation Deposit Account No. 09-0447.

B. REMARKS

During the July 3, 2006 interview, the rejections of claim 1 in view of *Kobayashi* (U.S. Patent No. 5,706,469) and claim 4 in view of *Kobayashi* and *Kirk, III* (U.S. Patent No. 6,421,690) were discussed. Applicants noted that *Kobayashi* neither teaches nor suggests Applicants' claimed feature (from independent claim 1) of a receiving a memory allocation request from a running process, where the request includes data relating to the size of the block of memory required and an indication of a mask bit pattern. The *Kobayashi* invention does not receive or process allocation requests, because *Kobayashi* deals only with the hardware interfacing of memory components and not with the allocation of memory for use by software. Applicants also pointed out that this feature was neither taught nor suggested by *Kirk, III*.

Further, with respect to the specific features recited in claim 4, Applicants pointed out that *Kirk III* also fails to teach the claimed feature of "embedding one or more bits of metadata into the address of the allocated block of memory." *Kirk III* differs from the present invention as recited in claim 4, because *Kirk III* teaches creating a complex pointer data structure containing additional data fields that are *separate* from the actual address stored in the pointer, rather than embedding metadata bits into the address itself, as recited in Applicants' claim 4.

Respectfully submitted,

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